•		No.
	Application No.	Applicant(s)
Notice of Allowability	10/811,488	YOSHIDA ET AL.
	Examiner	Art Unit
	DiLinh Nguyen	2814
		- I
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>5/30/06</u> .		
2. The allowed claim(s) is/are <u>1-8</u> .		
3.		
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/	6. ☐ Interview Summary Paper No./Mail Da	te
Paper No./Mail Date 4.		ent of Reasons for Allowance
of Biological Material	9. ☐ Other	HOAI PHAM
		HOAI PHAM PRIMARY EXAMINER

U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05) Application/Control Number: 10/811,488

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## **DETAILED ACTION**

## **REASONS FOR ALLOWANCE**

Claims 1-8 are allowed.

The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose the combination of a semiconductor device having a multilayer wiring structure recited in the base claim, including the combination of the structure comprising: a third power line from which a part corresponding to a position of the bypass capacitor is removed; a fourth power line from which a part corresponding to the position of the bypass capacitor is removed; contacts for connecting the first power line with the third power line and the second power line with the fourth power line.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (571) 272-1712. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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